



LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

(AUTONOMOUS)

Accredited by NAAC with 'A' Grade, ISO 9001:2015 Certified Institution

Approved by AICTE, New Delhi and Affiliated to JNTUK, Kakinada

L.B.Reddy Nagar, Mylavaram-521230, Krishna Dist, Andhra Pradesh, India

DEPARTMENT OF EEE

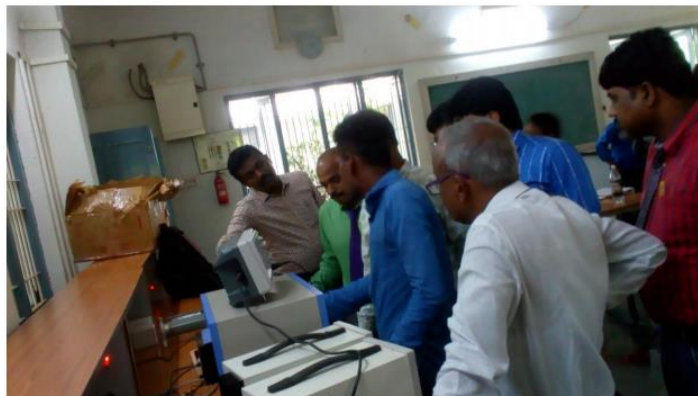
REPORT ON EVENT : DEMONSTRATION FPGA CONTROLLED DC-DC BUCK CONVERTER

- Event Type : DEMONSTRATION ACTIVITY ON LAB EQUIPMENT
- Date / Duration : 23-06-2017/One day
- Resource Person : Mr Ashok Kumar From Emsys Technologies
- Name of Coordinator : -----
- Target Audience : EEE Faculty
- Total no of Participants: -----
- Objective of the event: To get familiar with FPGA Controlled Dc-Dc Buck Converter
- Outcome of event : Working of the FPGA controlled dc-d buck converter for M.Tech Drives lab purpose and this facility can also be useful for faculty to do research work .

Description / Report on Event:

The department of EEE has organized new equipment demonstration activity to all faculty members in the department on 23-06-2017. The FPGA controlled dc-d buck converter has been procured from EMSYS Technologies, Coimbatore under Power Converters & Drives lab for M.Tech Drives lab purpose and this facility can also be useful for faculty to do research. The technical engineer Mr.Ashok Kumar from EMSYS technologies has demonstrated the equipment and working quite satisfactorily. This converter kit is having two control mechanisms namely PI control and hysteresis current control. Based on the selected reference output voltage, the converter generates the actual output voltage with acceptable ripple content. The supplier has also handed over the FPGA dumping kit for future research work. All the faculty members are trained the usage of kit and also code dumping mechanism with the help of FPGA controller.

Feedback / Suggestions :



Photographs :

Demo By Technical Engineer by Ashok Kumar.

